

REMARKS

Claims 1-16 are pending in this application, none of which have been amended. No new claims have been added.

These specification amendments, among other things, correct the spelling of the name of the author of two prior art documents cited in the specification. Although the English translation of the titles of these documents vary slightly from those of documents "AK" and "AL" listed in the IDS filed January 31, 2001, these documents do correspond to the documents cited in the IDS.

The claims are now in condition for examination.

Attached hereto is a marked-up version of the changes made to the specification and claims by the current amendment. The attached page is captioned "**VERSION WITH MARKINGS TO SHOW CHANGES MADE**".

U.S. Patent Application Serial No. 09/745,996

The Commissioner is authorized to charge our Deposit Account No. 01-2340 for any fee which is deemed by the Patent and Trademark Office to be required to effect consideration of this statement.

Respectfully submitted,

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Enclosure: Version With Markings To Show Changes Made

VERSION WITH MARKINGS TO SHOW CHANGES MADE

In the Specification:

Heading beginning at line 11 of page 1 has been amended as follows:

Description of the [Background] Related Art

Paragraph beginning at line 1 of page 3 has been amended as follows:

Digital matched filter 71 is employed because of its superior peak detection speed for a correlation value between a received signal and a spreading code. The digital matched filter is discussed in detail for example by [Tajika] Tachika in "Digital Matched Filter Technique in Spread Spectrum Communication and its Problems," *IEICE Technical Report* SST 62-21.

Paragraph beginning at line 28 of page 3 has been amended as follows:

Then, the accuracy of peak detection of correlation values can be improved by averaging correlation values of multiple frames even if E_b/N_o is small. A digital matched filter using this cyclic integration is discussed in detail, for example, by [Tajika] Tachika et al in "DS/GMSK/PSK System Using Four-Phase Correlator and Spread Spectrum Demodulation LSI," *IEICE Technical Report* SST96-26.